

CURRICULUM VITAE

NICOLA ANDRIOLLI

National Research Council of Italy (CNR)
Institute of Electronics, Information Engineering and Telecommunications (IEIIT)

Via Caruso 16
56122 Pisa - Italy

email: nicola.andriolli@ieiit.cnr.it
web: <https://www.ieiit.cnr.it/people/Andriolli-Nicola>

EDUCATION

- 2003 – 2006 “Perfezionamento” degree (**PhD**), Telecommunications Engineering
Scuola Superiore Sant’Anna; marks 100/100 with honors
Dissertation: “Advanced architectures and protocols for optical networks”
Supervisor: Prof. ██████████
- 1997 – 2003 “Licenza Ordinaria” degree (**subsidiary MS**), Telecommunications Engineering
Scuola Superiore Sant’Anna; marks 100/100 with honors.
- 1997 – 2002 “Laurea” degree (**MS**), Telecommunications Engineering
University of Pisa; marks 110/110 with honors.

PROFESSIONAL AND ACADEMIC EXPERIENCE

- 2019 – present **Researcher**
National Research Council of Italy (CNR-IEIIT)
- 2007 – 2019 **Assistant Professor**
Scuola Superiore Sant’Anna
- 2006 – 2007 **Postdoctoral Fellow**
Scuola Superiore Sant’Anna

HONOURS, AWARDS, AND RECOGNITIONS

- 2011 **Premio Venezia Award**, Italian Chamber of Commerce in Canada
- 2005 **Outstanding Graduation Award**, University of Pisa, Course of Study in
Telecommunications Engineering

PROFESSIONAL QUALIFICATIONS

- 2020 National Scientific Qualification for Full Professor, Telecommunications Engineering
- 2019 National Scientific Qualification for Associate Professor, Electronics
- 2017 National Scientific Qualification for Associate Professor, Telecommunications Engineering
- 2003 Qualification to the Engineer Professional Practice

PROFESSIONAL AFFILIATIONS

- 2008 – present **GTTI** (National Telecommunications and Information Technologies Group)
- 2004 – present **CNIT** (National Inter-University Consortium for Telecommunication)

PROFESSIONAL MEMBERSHIPS

IEEE (Senior Member)

PROFESSIONAL ACTIVITIES

- 2022 **Technical Program Committee (TPC) member**, IEEE International Conference on
Communications (ICC2022), Symposium on “Optical Networks and Systems”

- 2021 **Technical Program Committee (TPC) member**, IEEE Global Communications Conference (Globecom2021), Symposium on “Next-Generation Networking and Internet”
- 2020 **TPC member**, International Conference on Photonics in Switching and Computing (PSC 2020), Subcommittee on “Photonics in computing systems and deep learning application”
- 2019 – present **Associate Editor**, IEEE Access (IF=4.098, 2018 JCR)
- 2018 **TPC member**, 44th European Conference on Optical Communication (**ECOC**), Subcommittee 6 on “Datacentre and Computercom Networks”
- 2017 – present **TPC member**, European Conference on Networks and Communications (**EuCNC**)
- 2016 **TPC member**, OSA Meeting on Advanced Photonics - Integrated Photonics Research, Silicon, and Nano-Photonics (**IPR**)
- 2015 – present **Technical Reviewer for the European Commission and Italian Ministry of University and Research: FP7 Integrated Project, Horizon 2020 Research and Innovation Action, JU ECSEL Innovation Action, Young Researcher Program “Rita Levi Montalcini”, FISIR2020.**
- 2004 – present **Reviewer**, IEEE/OSA J. Lightw. Technol., OSA Opt. Express, IEEE Photon. Technol. Lett., IEEE Commun. Lett., IEEE J. Sel. Areas Commun., IEEE J. Sel. Topics Quantum Electron., IEEE Photon. J., IEEE Access, Advances in Physics: X, OSA Continuum, IEEE/OSA J. Opt. Commun. Netw., J. Opt. Netw., Photonics Research, Opt. Commun., Appl. Sci., Electronics, IET Commun., Comput. Netw., Nano Commun. Netw.

RESEARCH INTERESTS

Optical integration technologies for datacom and telecom applications, design and characterization of optical components in silicon-on-insulator / silicon nitride / indium phosphide platforms, optical signal processing, optical comb generation, optical interconnection network architectures and scheduling, design and performance analysis of optical circuit- and packet-switched networks and nodes, neuromorphic photonics, quantum integrated photonics.

PUBLICATIONS AND PATENTS

He published 190 papers (source Scopus, Jul. 2021) in International Journals and Conference Proceedings. He is an inventor of 11 international patents. Research indices:

Google Scholar	Scopus
<ul style="list-style-type: none">• Citations: 1732• h-index: 22	<ul style="list-style-type: none">• Citations: 1207• h-index: 19

Google Scholar: <https://scholar.google.com/citations?user=WMz-bI8AAAAJ>

Scopus: <https://www.scopus.com/authid/detail.uri?authorId=14010786900>

Publons: <https://publons.com/researcher/2713574/nicola-andriolli/>

Google Patents: <https://patents.google.com/?inventor=Nicola+Andriolli>

PROJECTS AND GRANTS

Dr. Andriolli participated to several EC, Italy, and Tuscany Region funded projects. He has been the Italian coordinator of three high-relevance bilateral projects with McGill University funded by Italian Ministry of Foreign Affairs and the Quebec Ministry of Economic Development, Innovation and Export, and the research unit coordinator of FIRB project funded by the Italian Ministry of University and Research. He was involved in several collaborations with national and foreign companies (e.g., Italian Railway Networks, GE, Ericsson).

TEACHING

Dr. Andriolli advised several Master and Ph.D. students, and taught courses on Network Software, Design of Access, Metro and Core Networks, Interconnection Networks, Data Center Networks, Photonic Integrated Circuits.